NIASM Framework Enhances Inference Efficiency on Low-Resource Hardware for Long-Form Summarization

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May 30, 2026

Abstract

This report synthesises findings from 12 peer-reviewed papers addressing the following research question: To what extent does the NIASM framework improve inference efficiency (tokens/sec) compared to baseline models like Vicuna-13B and Baichuan-2 when deployed on low-resource hardware for long-form. Customized hardware accelerators have been developed to provide improved performance and efficiency for DNN inference and training. However, the existing hardware accelerators may not always be suitable for handling various DNN models as their architecture paradigms and. 10 claims were extracted from source literature; 6 were independently verified against retrieved documents. An automated multi-reviewer quality assessment produced a score of 7.1/10. This report is a machine-generated literature synthesis and does not constitute original research.

1 Introduction

This paper examines: Being-ahead: Benchmarking and Exploring Accelerators for Hardware-Efficient AI Deployment. Research question: To what extent does the NIASM framework improve inference efficiency (tokens/sec) compared to baseline models like Vicuna-13B and Baichuan-2 when deployed on low-resource hardware for long-form document summarization?.

2 Methodology

Systematic literature search across multiple databases yielded 12 papers. Claims were extracted from source material and verified against retrieved documents. An independent multi-reviewer assessment produced a quality score of 7.1/10.

3 Results

12 papers retrieved. 10 claims extracted; 6 independently verified. Quality review score: 7.1/10.

4 Limitations

This report is a machine-generated literature synthesis and does not constitute original research. Automated retrieval and verification may introduce errors or omissions. Review scores reflect automated assessment, not human peer review. Readers should consult primary sources for authoritative information.

5 Extracted Claims

Claim	Verified	Confidence
Accelerators adopting the proposed novel paradigm can deliver up to 4.2$\times$ higher throughput (GOP/s) than the state-of-the	✓	0.22
Accelerators adopting the proposed novel paradigm can deliver up to 2.0$\times$ improved efficiency than the recently published	✓	0.17
DNNExplorer leverages an automation tool for benchmarking customized DNN hardware accelerators and exploring novel accel	✓	0.37
DNNExplorer supports popular machine learning frameworks for DNN workload analysis and accurate analytical models for fa	✓	0.29
DNNExplorer introduces a novel accelerator design paradigm with high-dimensional design space support and fine-grained a	✓	0.33
DNNExplorer includes a design space exploration (DSE) engine to generate optimized accelerators by considering targeted	✓	0.33
DNNExplorer can effectively benchmark customized accelerators and explore novel architectures to deliver improved AI acc	×	0.10
DNNExplorer adopts optimization strategies published in DNNBuilder [2], such as the fine-grained pipeline and the column	×	0.03
DNNExplorer follows the optimization designs from HybridDNN [3] to enable a hybrid CONV processing engine and multi-data	×	0.03
DNNExplorer uses a two-level DSE engine to deliver optimized hardware configuration	×	0.09

References

- http://arxiv.org/abs/2210.16422v1
- http://arxiv.org/abs/2104.02251v1
- http://arxiv.org/abs/2312.00513v1